

IN THE CLAIMS:

Please cancel claims 1-5, 30-46 and 51-54 without prejudice or disclaimer to file a divisional reissue application directed thereto.

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[1. A liquid crystal display device comprising:
first and second substrates each having an insulating surface;
at least one pixel electrode arranged on the first substrate at a matrix form;
an opposite electrode arranged on the second substrate;
a liquid crystal disposed between the first and second substrates; and
at least one digital memory circuit which is constructed by thin film transistors,
wherein the digital memory circuit is connected with the pixel electrode and stores a binary
information;
wherein a voltage is directly applied to said pixel electrode through said digital
memory circuit based on said binary information,
wherein said liquid crystal display device includes a time gradation display device.]

[2. The device of claim 1 wherein a voltage having an amplitude equivalent to
that of the voltage stored in the digital memory circuit is supplied to the opposite electrode.]

[3. The device of claim 1 wherein the number of pixel electrodes equals the
number of the digital memory circuits.]

[4. The device of claim 1 wherein the liquid crystal display device includes a
digital gradation display device.]

[5. The device of claim 1 wherein the voltage includes one of a high voltage and a
low voltage.]

30. [Canceled]
31. [Canceled]
32. [Canceled]

33. [Canceled]
34. [Canceled]
35. [Canceled]
36. [Canceled]
37. [Canceled]
38. [Canceled]
39. [Canceled]
40. [Canceled]
41. [Canceled]
42. [Canceled]
43. [Canceled]
44. [Canceled]
45. [Canceled]
46. [Canceled]
51. [Canceled]
52. [Canceled]
53. [Canceled]
54. [Canceled]